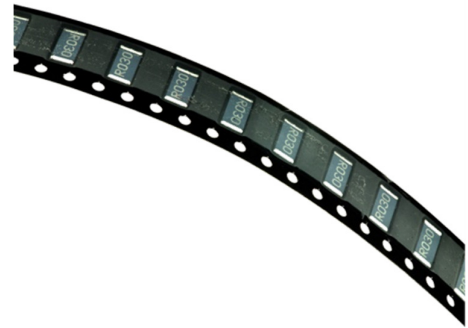


Packing Specification PS003 - Specialist Chip Resistors



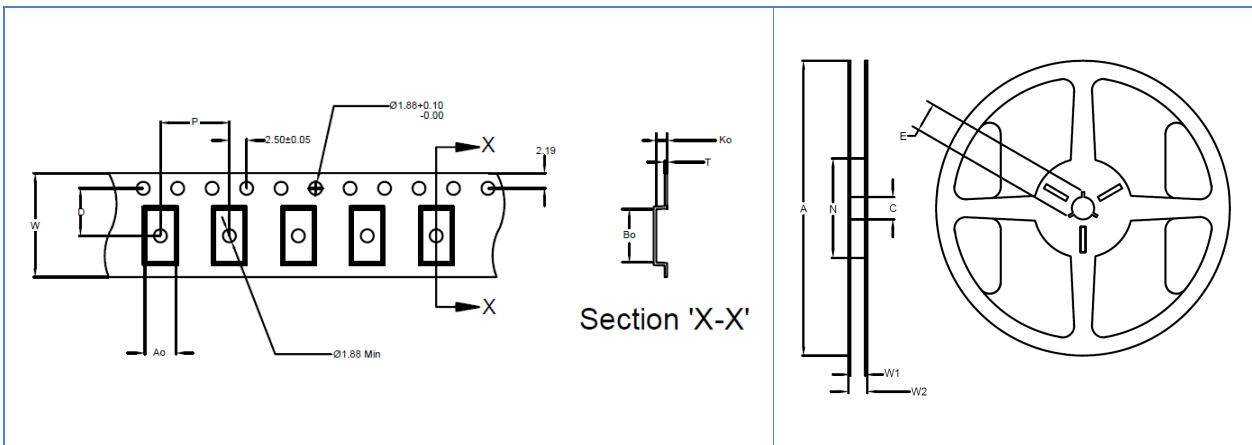
Applicable Series

CR, DPCR, DSC, GHVC, HDSC, HPWC, HR, HVC, LR(F), LRF3W, LRZ, PWC, SC3, TJC

This packing specification covers all chip sizes and only solderable termination versions. See individual product datasheets for sizes available in each series.

Products are packed in embossed plastic tape with the dimensions shown below.

Dimensions in mm



Size	Qty ¹	A ₀	B ₀	K ₀	P	T	W	D	A	C	E	N	W ₁	W ₂
0603	5000	0.90 ±0.05	1.83 ±0.05	0.68 ±0.05	4.0 ±0.1	0.20 ±0.03	8.0 ±0.2	3.50 ±0.05	178 ±2	13.5 ±0.5	20.2 min.	60 nom.	9.0 ±0.5	11.5 max.
0805/0508	3000	1.60 ±0.05	2.41 ±0.05	0.81 ±0.05		0.25 ±0.05	8.0							
1206/0612		1.85 ±0.1	3.56 ±0.1	1.04 ±0.1	0.23 ±0.05	+0.3/-0.1								
2010/1020		2.85 ±0.1	5.55 ±0.1	0.85 ±0.1	0.25 ±0.05	12.0								
2512/1225	1800	3.70 ±0.1	6.90 ±0.1	1.25 ±0.1	8.0 ±0.1	0.30 ±0.05	+0.3/-0.1	5.50 ±0.05				13.0 ±0.5	15.5 max.	

Note 1: These are standard maximum quantities. Some products may be available with lower reel quantities – consult product datasheet for details.

Note 2: Dimensions and materials conform to EIA-481 standard.

Note 3: Cavity tape is conductive black polycarbonate. Cover tape is polyester film with antistatic coating.

General Note TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of publication.