Surface Mount NPN General Purpose Transistor

2N2222AUA 2N2222AUATXV - NRND 2N2222AUATX - Obsolete

Features:

- Ceramic 4 pin surface mount package
- Small package to minimize circuit board area
- Hermetically sealed
- Processed per MIL-PRF-19500/255

Description:

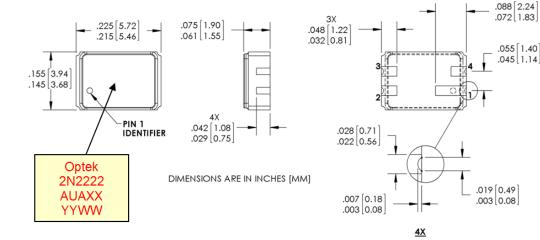
The 2N2222AUA (TX - Obsolete, TXV - NRND) is a hermetically sealed ceramic surface mount general purpose switching transistor. The four pin ceramic package is ideal for designs where board space and device weight are important design considerations. The "UA" suffix denotes the 4 terminal leadless chip carrier package, type "A" per MIL-PRF-19500/255.

Typical screening per MIL-PRF-19500/255. The burn-in condition is V_{CB} = 30 V. P_D = 400 mW, T_A = 25° C, t = 80 hrs. Refer to MIL-PRF-19500/255 for complete requirements. In addition, the TX and TXV versions receive 100 % thermal response testing.

When ordering parts without processing, do not use the TX or TXV suffix.

Applications:

- General switching
- Amplification
- Signal processing
- Radio transmission
- Logic gates



Pin	Function				
1	Collector				
2	Emitter				
3	Base				
4	No Connection				



TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

TT Electronics | OPTEK Technology 2900 E. Plano Pkwy, Plano, TX 75074 | Ph: +1 972 323 2200 www.ttelectronics.com | sensors@ttelectronics.com





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Absolute Maximum Ratings (T _A = 25° C unless otherwise noted)	
Collector-Base Voltage	75 V
Collector-Emitter Voltage	50V
Emitter-Base Voltage	6.0 V
Collector Current-Continuous	800 mA
Operating Junction Temperature (T _J)	-65° C to +200° C
Storage Junction Temperature (T _{stg})	-65° C to +200° C
Power Dissipation @ $T_A = 25^{\circ} C$	0.5 W
Power Dissipation @ Tc = 25° C	1.16 W ⁽¹⁾
Soldering Temperature (vapor phase reflow for 30 seconds)	215° C
Soldering Temperature (heated collet for 5 seconds)	260° C

SYMBOL	PARAMETER	MIN	MAX	UNITS	TEST CONDITIONS
OFF CHAR	ACTERISTICS				
V _{(BR)CBO}	Collector-Base Breakdown Voltage	75		V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	50		V	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	6.0		V	$I_{E} = 10 \ \mu A, \ I_{C} = 0$
I _{CBO}	Collector-Base Cutoff Current		10	nA	$V_{CB} = 60 \text{ V}, I_E = 0$
			10	μΑ	V_{CB} = 60 V, I_{E} = 0, T_{A} = 150° C
I _{EBO}	Emitter-Base Cutoff Current		10	nA	$V_{EB} = 4 V, I_{C} = 0$
I _{CES}	Collector Emitter Cutoff Current		50	nA	V _{CE} = 50 V
ON CHAR	ACTERISTICS				
h _{FE}	Forward-Current Transfer Ratio	50		-	V_{CE} = 10 V, I _C = 0.1 mA
		75	325	-	V_{CE} = 10 V, I _C = 1.0 mA
		100		-	V_{CE} = 10 V, I _C = 10 mA
		100	300	-	V_{CE} = 10 V, I_{C} = 150 mA ⁽²⁾
		30		-	V_{CE} = 10 V, I_{C} = 500 mA ⁽²⁾
		35		-	$V_{CE} = 10 \text{ V}, I_{C} = 10 \text{ mA}, T_{A} = -55^{\circ} \text{ (}$

Note:

1. Derate linearly 6.6 mW/° C above 25° C

2. Pulse Width ≤300 μs , Duty Cycle ≤ 2.0 %

General Note

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SYMBOL	PARAMETER	MIN	MAX	UNITS	TEST CONDITIONS
ON CHAR	ACTERISTICS				
V _{CE (SAT)}	Collector-Emitter Saturation Voltage		0.3	V	$I_{\rm C}$ = 150 mA, $I_{\rm B}$ = 15 mA ⁽²⁾
			1.0	V	$I_{\rm C}$ = 500 mA, $I_{\rm B}$ = 50 mA ⁽²⁾
$V_{\text{BE(SAT)}}$		0.6	1.2	V	$I_{\rm C}$ = 150 mA, $I_{\rm B}$ = 15 mA ⁽²⁾
	Base-Emitter Saturation Voltage		2.0	V	$I_{\rm C}$ = 500 mA, $I_{\rm B}$ = 50 mA ⁽²⁾
SMALL-SIC	GNAL CHARACTERISTICS				
h _{fe}	Small Signal Forward Current Transfer Ratio	50		-	V _{CE} = 10 V, I _C = 1.0 mA, f = 1.0 kHz
h _{fe}	Small Signal Forward Current Transfer Ratio	2.5		-	V _{CE} = 20 V, I _C = 20 mA, f = 100 MHz
C _{obo}	Open Circuit Output Capacitance		8.0	pF	V_{CB} = 10 V, 100 kHz \leq f \leq 1.0 MHZ
C _{ibo}	Input Capacitance (Output Open)		25	pF	V_{EB} = 0.5 V, 100 kHz \leq f \leq 1.0 MHZ
SWITCHIN	G CHARACTERISTICS				
t _{on}	Turn-On Time		35	ns	$V_{CC} = 30 \text{ V}, \text{ I}_{C} = 150 \text{ mA}, \text{ I}_{B1} = 15 \text{ mA}$
t _{off}	Turn-Off Time		300	ns	$V_{CC} = 30 \text{ V}, I_{C} = 150 \text{ mA}, I_{B1} = I_{B2} = 15 \text{ mA}$