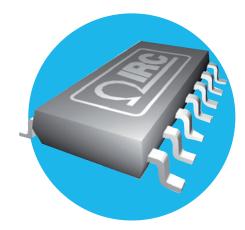
Resistors OBSOLETE

Surface Mount TVS Diode Array

QDN005LF Series

- Protects up to 8 data lines
- Low operating and clamping voltages
- Working voltages from 5 to 24V
- Low leakage current
- RoHS compliant





All Pb-free parts comply with EU Directive 2011/65/EU (RoHS2)

Applications

RS-232 and RS-432 Data Lines Desktop and Portable Computers Microprocessor Based Equipment LAN/WAN Communication Equipment Wireless Communication Equipment

Characteristic Data

IEC Compatibility

61000-4-2 (ESD): Air ±15kV, Contact ±8kV 61000-4-4 (EFT): 40A - 5/50ns 61000-4-5 (Lightning): 12A, 8/20μs

	Rated Reverse Standoff Voltage V _{RWM} (Volts)	Minimum Reverse Breakdown Voltage $V_{(BR)}$ $I_t = 1mA$ (Volts)	Maximum Clamping Voltage V_c $I_p = 1A$ (Volts)	Maximum Pulse Current I_{pp} $t_p = 8/20\mu s$ (A)	Maximum Reverse Leakage Current Ι _R @V _{RWM} (μΑ)	Maximum Capacitance C V=0 f = 1MHz (pF)
	(10113)	(10113)	(10113)	(4)	(µA)	(p)
QDN005LF-05	5.0	6.0	9.8	17.0	20	350
QDN005LF-12	12.0	13.3	19.0	12.0	1	120
QDN005LF-15	15.0	16.7	24.0	10.0	1	75
QDN005LF-24	24.0	26.7	43.0	5.0	1	50

Maximum Ratings

Peak Pulse Power	Operating	Storage	Solder Temperature
(t _p = 8/20µs)	Temperature	Temperature	(10 seconds)
P _{pp}	T _j	Т _{sтg}	T _{II}
300W	-55°C to +150°C	-55°C to +150°C	260°C

General Note

TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

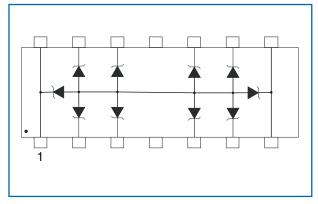
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Surface Mount TVS Diode Array QDN005LF Series

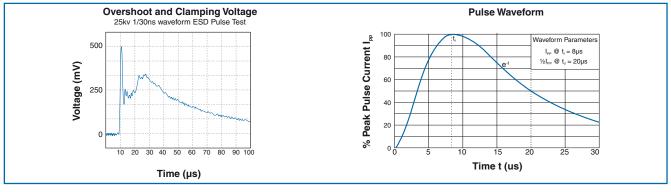
Schematic Data



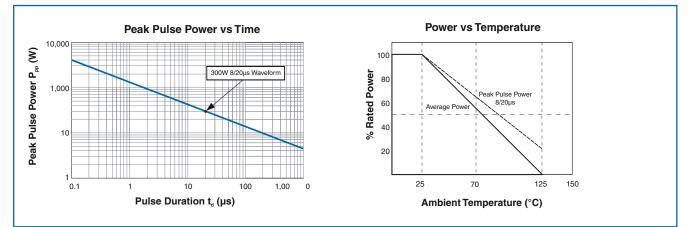
J					
	Initial Peak Current	Peak Current 30ns	Peak Currnet 60ns	Test Voltage Contact	Test Voltage Air
Level	(A)	(A)	(A)	(kV)	(kV)
1	7.5	4.0	8.0	2.0	2.0
2	15.0	8.0	4.0	4.0	4.0
3	22.5	12.0	6.0	6.0	8.0
4	30.0	16.0	8.0	8.0	15.0

Discharge Data (IRC 61000-4-2)

ESD Pulse Waveform Data



Power Rating Data



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Surface Mount

TVS Diode Array

Physical Data

0.050" Typ -

0.016" ± .002"-

QDN005LF Series

All dimesions exclude mold flash, end flash and metal burrs which shall not exceed 0.015" per side. Maximum lead coplanarity is 0.004".

D

IRC

Ordering Data

Prefix·····	···· DNR -	QDN005LF	- 05 - TO	07
Model . QDN005LF = TVS Array in a 14-Pin SOIC-N Package				:
Characteristic Code 05 = 5.0V Reverse Standoff Voltage 12 = 12.0V Reverse Standoff Voltage 15 = 15.0V Reverse Standoff Voltage 24 = 24.0V ReverseStandoff Voltage				• • • • • • • • • • •
Packaging				:



н

A

0.007" ±0.003"

0° min / 8° max



QDN005

0.341"±0.004

0.236"±0.008

0.153"±0.004

0.061"±0.008

0.008"±0.002

.033"±0.017

D

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С

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