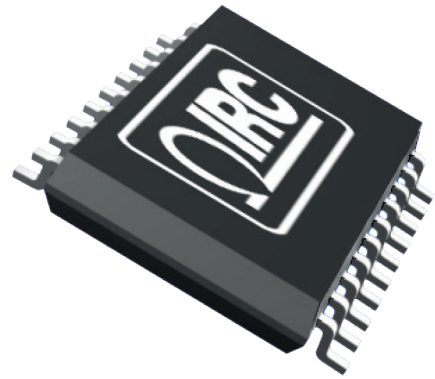


Surface Mount TSSOP Resistor Networks

TSSOP Series

- Absolute tolerances to $\pm 0.1\%$
- Tight TC Tracking to $\pm 5\text{ppm}/^\circ\text{C}$
- Ratio match tolerances to $\pm 0.05\%$
- Ultra-stable tantalum nitride resistors
- Standard Sn/Pb and Pb-free terminations available



 All Pb-free parts comply with EU Directive 2011/65/EU amended by (EU) 2015/863 (RoHS3)

IRC's TaNSil® TSSOP resistor networks are ideally suited for high volume applications that demand a small, low-profile footprint. The small wire-bondable chip package provides higher component density, lower resistor cost and high reliability.

The tantalum nitride film system on silicon provides precision tolerance, exceptional TCR tracking, low cost and miniature package. Excellent performance in harsh, humid environments is a trademark of IRC's self-passivating TaNSil® resistor film.

For applications requiring high performance resistor networks in a low cost, low profile, surface mount package, specify IRC TSSOP resistor networks.

Electrical Data

Resistance Range	10 Ω to 250K Ω
Absolute Tolerance	To $\pm 0.1\%$
Ratio Tolerance to R1	To $\pm 0.05\%$
Absolute TCR	To $\pm 25\text{ppm}/^\circ\text{C}$
Tracking TCR	To $\pm 5\text{ppm}/^\circ\text{C}$
Element Power Rating @ 70°C	
Isolated Schematic	100mW
Bussed Schematic	50mW
Package Power Rating @ 70°C	
16-Pin	800mW
20-Pin	1.0W
24-Pin	1.0W
Rated Operating Voltage (not to exceed $\sqrt{P \times R}$)	100 Volts
Operating Temperature	-55°C to +125°C
Noise	<-30dB

Environmental Data

Test Per MIL-PRF-83401	Typical Delta R	Max Delta R
Thermal Shock	$\pm 0.02\%$	$\pm 0.1\%$
Power Conditioning	$\pm 0.03\%$	$\pm 0.1\%$
High Temperature Exposure	$\pm 0.03\%$	$\pm 0.5\%$
Short-time Overload	$\pm 0.02\%$	$\pm 0.5\%$
Low Temperature Storage	$\pm 0.03\%$	$\pm 0.5\%$
Life	$\pm 0.05\%$	$\pm 0.1\%$

General Note

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TSSOP Series

Manufacturing Capabilities

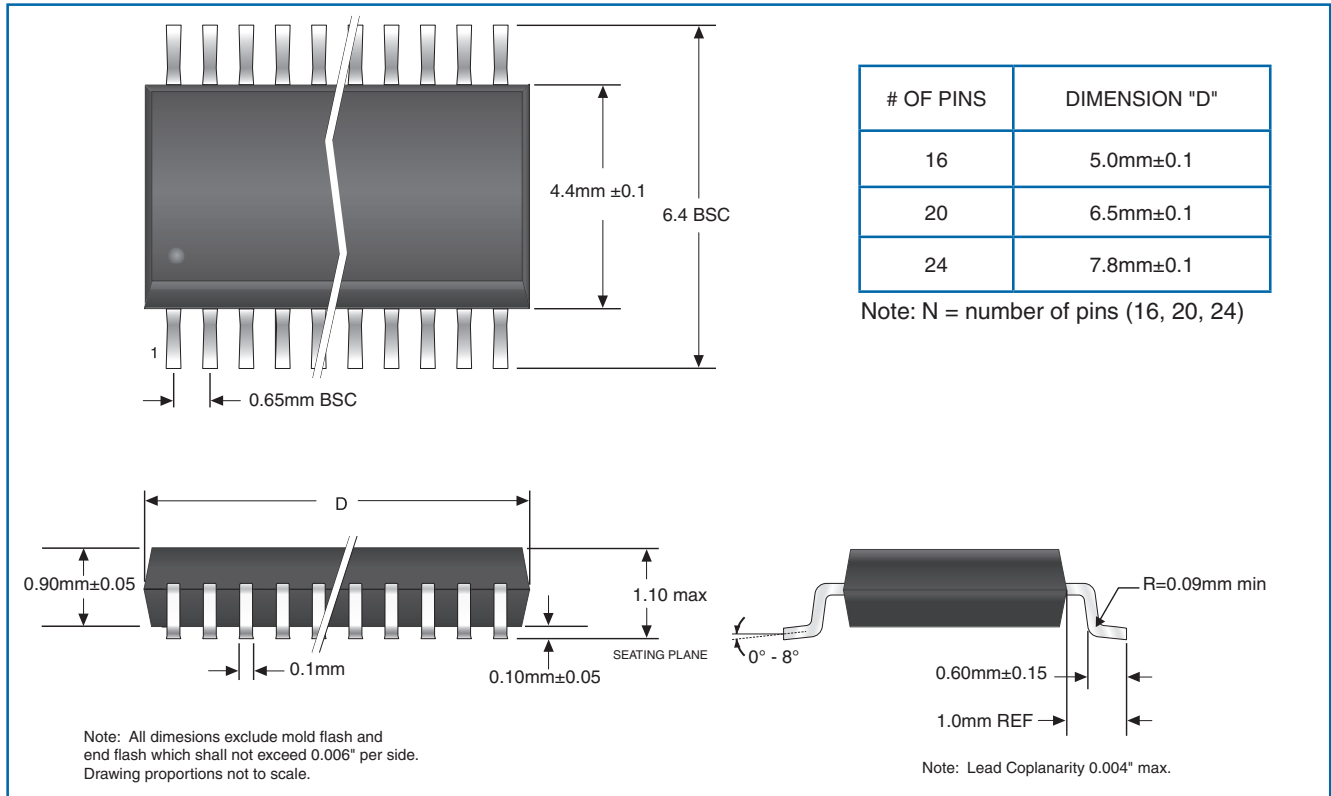
Absolute TCR (±ppm/°C)	ISOLATED SCHEMATIC A				BUSSED SCHEMATIC B			
	Ohmic Range (Ω)	Available Tolerances	Available Ratio Tolerances	Best Tracking (±ppm/°C)	Ohmic Range (W)	Available Tolerances	Available Ratio Tolerances	Best Tracking (±ppm/°C)
250	10 - 25	F G J	F G	100	10 - 25	F G J	F G	200
	26 - 50	D F G J	C D F G	50	26 - 50	F G J	D F G	100
	51 - 200	C D F G J	C D F G	10	51 - 100	D F G J	C D F G	50
	201 - 250K	B C D F G J	A B C D F G	5	101 - 200	D F G J	B C D F G	25
					201 - 500	B C D F G J	B C D F G	20
					501 - 100K	B C D F G J	A B C D F G	5
100	26 - 50	D F G J	C D F G	50	26 - 50	F G J	D F G	100
	51 - 200	C D F G J	C D F G	10	51 - 100	D F G J	C D F G	50
	201 - 250K	B C D F G J	A B F G	5	101 - 200	D F G J	B C D F G	25
					201 - 500	B C D F G J	B C D F G	20
					501 - 100K	B C D F G J	A B C D F G	5
50	26 - 50	D F G J	C D F G	50	51 - 100	D F G J	C D F G	50
	51 - 200	C D F G J	C D F G	10	101 - 200	D F G J	B C D F G	25
	201 - 250K	B C D F G J	A B F G	5	201 - 500	B C D F G J	B C D F G	20
					501 - 100K	B C D F G J	A B C D F G	5
25	51 - 200	C D F G J	C D F G	10	201 - 500	B C D F G J	B C D F G	20
	201 - 250K	B C D F G J	A B F G	5	501 - 100K	B C D F G J	A B C D F G	5

General Note

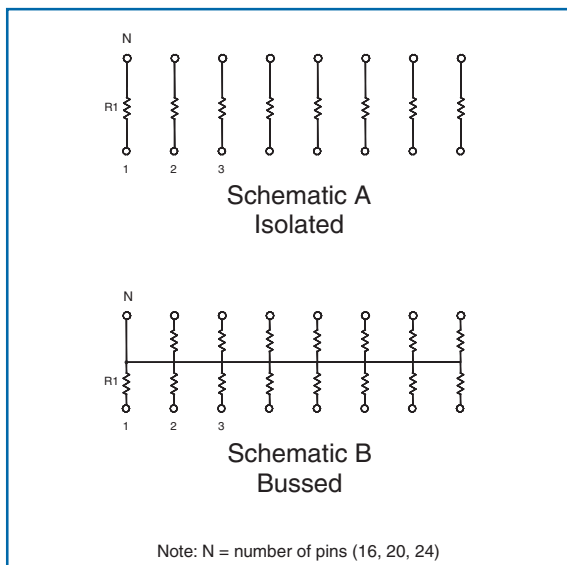
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TSSOP Series

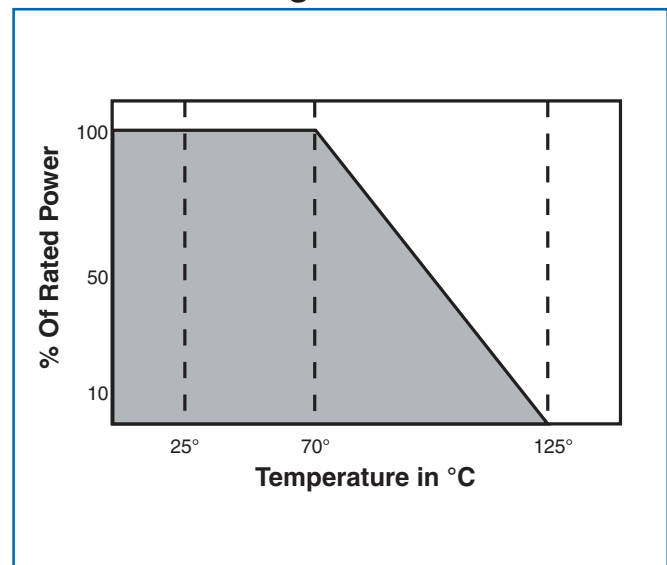
Physical Data



Schematic Data



Power Derating Curve

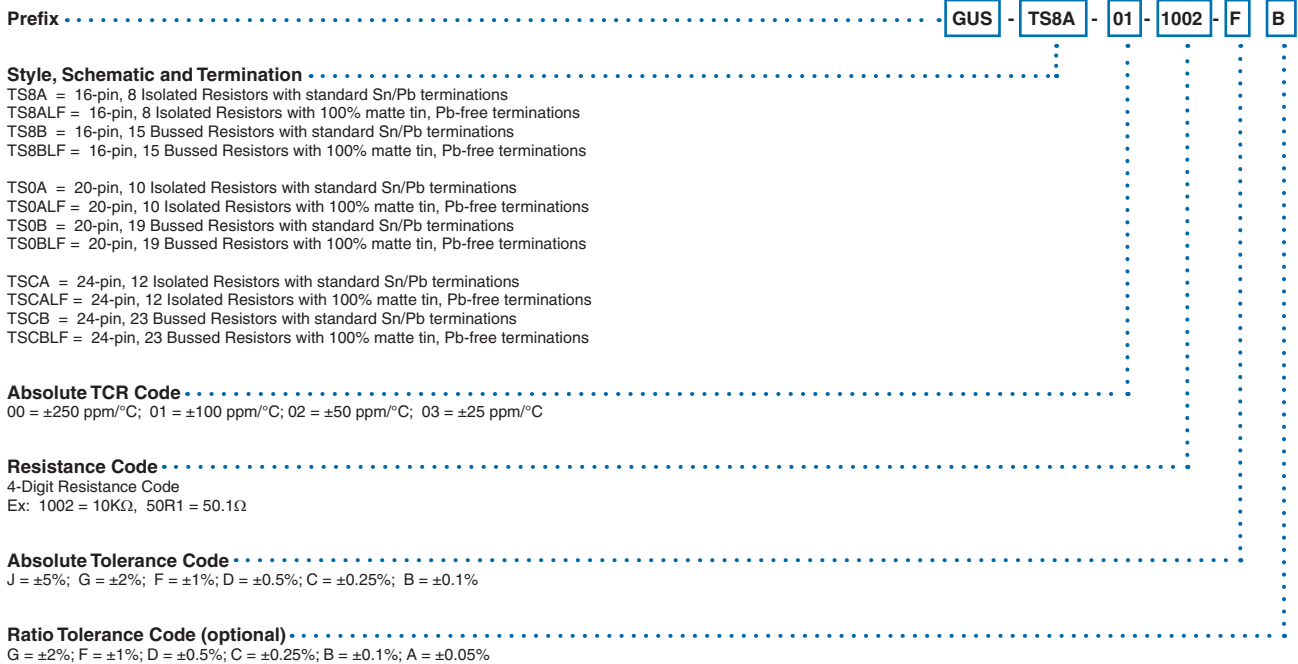


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TSSOP Series

Ordering Data



Packaging
 Specify tubes or tape & reel.

For additional information or to discuss your specific requirements, please contact our Applications Team using the contact details below.

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