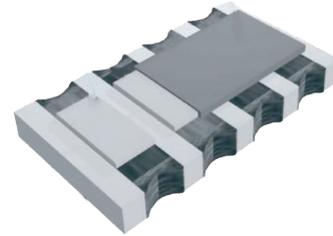


Thickfilm Chip Array Resistor Network

WCC Series

Features

- Monolithic construction
- Concave termination style
- Non-leaching nickel barrier wrap-around terminations
- Improved placement efficiency over flat chip resistors



OBSOLETE

All parts are Pb-free and comply with EU Directive 2011/65/EU amended by (EU) 2015/863 (RoHS3)

Electrical Data

Resistance Range	1% 22Ω - 1MΩ 5% 10Ω - 1MΩ zero ohm jumper
Operating Temperature Range	-55°C to +125°C
Temperature Coefficient	±200ppm/°C
Power Dissipation (@70°C)	0.65 Watts
Maximum Working Voltage	50 Volts

Environmental Data

Test*	Maximum ΔR
Thermal Shock	±1.0%
Short Time Overload	±1.0%
High Temperature Exposure	±3.0%
Resistance to Solder Heat	±1.0%
Moisture Resistance	±2.0%
Load Life	±3.0%
Low Temperature Operation	±0.5%
Solderability	95% Coverage

* Test methods per EIA-575

Physical Data

Dimensions (Inches and (mm))					
L	C	D	P	W	T
0.126 ± .008 (3.20 ± .20)	0.018 ± .004 (0.45 ± .10)	0.016 ± .006 (0.40 ± .15)	0.031 ± .008 (0.80 ± .20)	0.063 ± .008 (1.60 ± .20)	0.016 ± .004 (.40 ± .10)

General Note

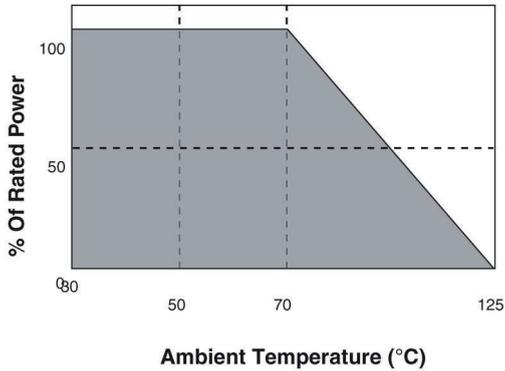
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BI Technologies IRC Welwyn

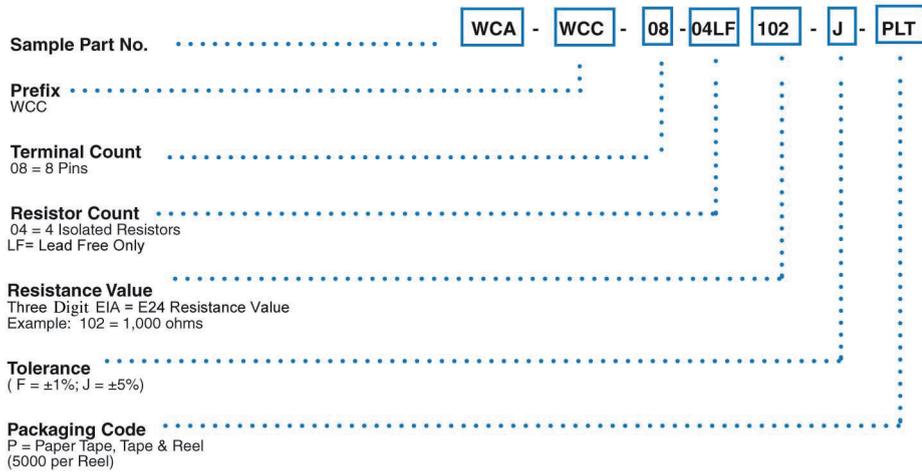
www.ttelectronics.com/resistors

WCC Series **OBSOLETE**

Power Derating Curve



Ordering Data



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